

X-1478 US  
10/787,326

PATENT  
Conf. No.: 3972



IN THE UNITED STATES PATENT OFFICE

Applicants: Guenter Stenz et al.  
Assignee: Xilinx, Inc.  
Title: Method System and Apparatus for Floorplanning Programmable Logic Designs  
Serial No.: 10/787,326 File Date: 02-26-04  
Examiner: Vuthe Siek Art Unit: 2825  
Docket No.: X-1478 US Conf. No.: 3972

---

Mail Stop AF  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

*NOT TO ENTER*

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

*VIS  
1-24-07*

Dear Sir:

In response to the Final Office Action mailed from the Patent Office on November 7, 2006, please replace/substitute the following sections / claims as indicated.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Amendments to the Drawings** begin on page 7 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

**Remarks** begin on page 8 of this paper.

An **Appendix** including an amended drawing figure is attached following page 9 of this paper.